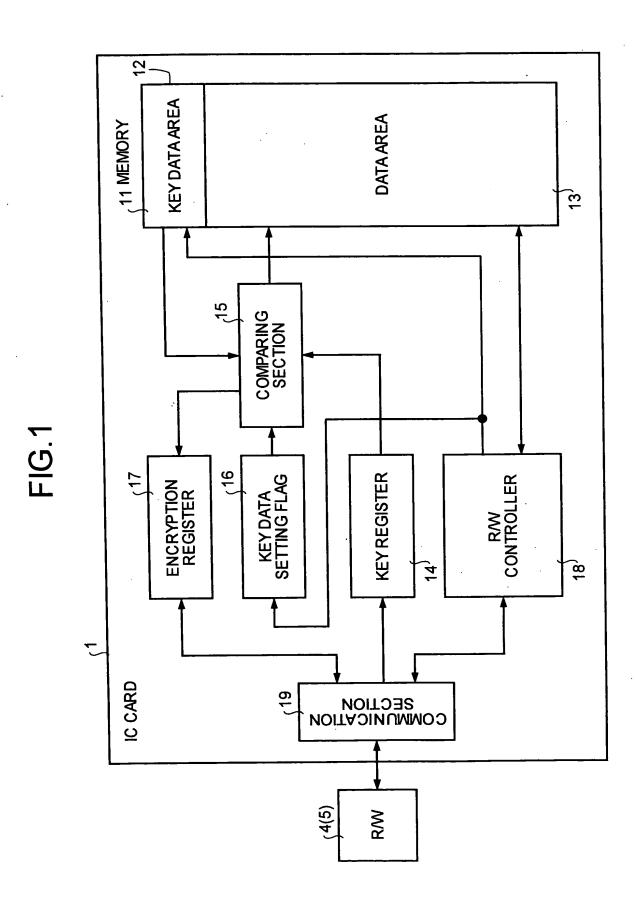
Title: MEMORY DEVICE, MEMORY ACCESS LIMITING SYSTEM, AND MEMORY ACCESS METHOD Inventor's Name: HASHIMOTO, et al. Application No.: NEW Docket No.: 108391-00038

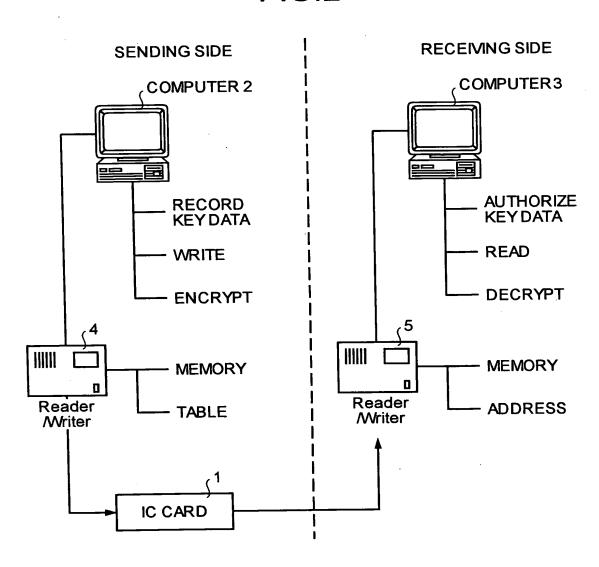
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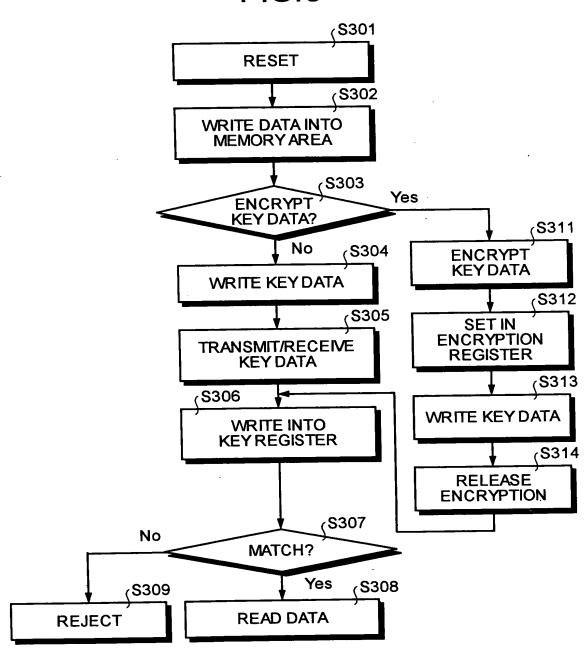
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FIG.2



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FIG.3



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FIG.4A

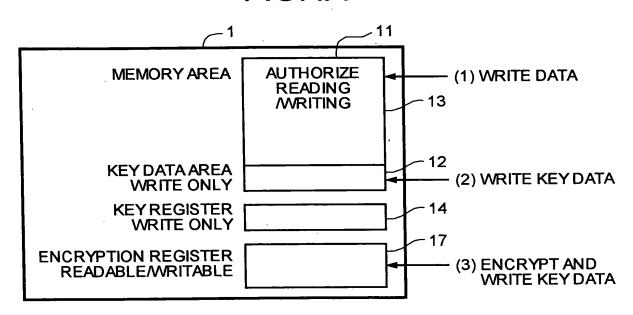
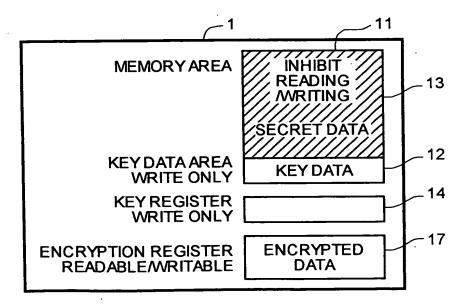


FIG.4B



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FIG.5A

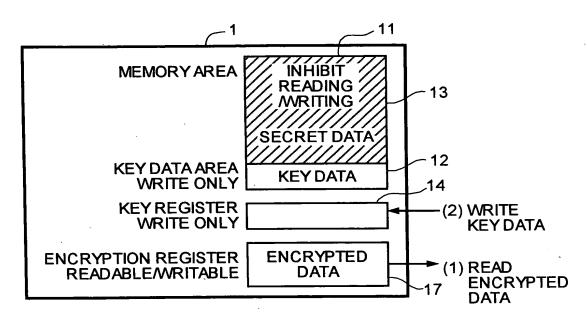
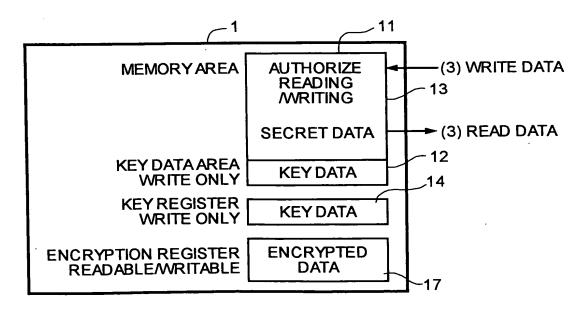


FIG.5B



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FIG.5C

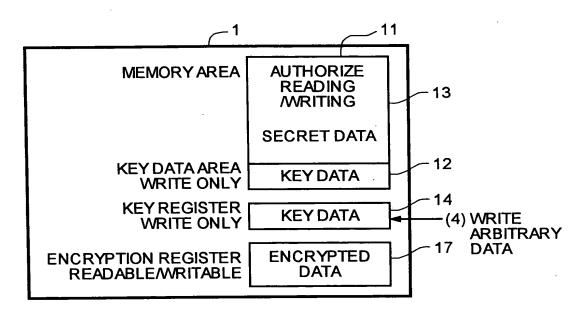
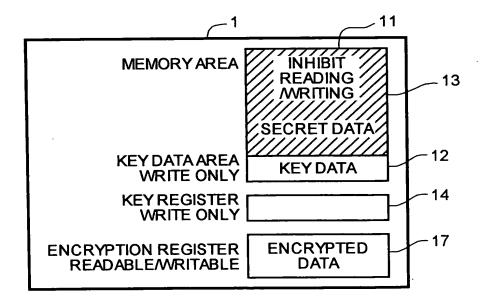


FIG.5D



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FIG.6

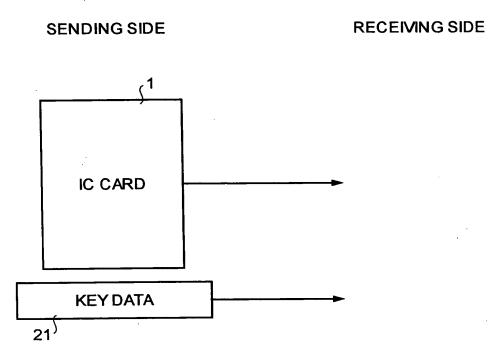
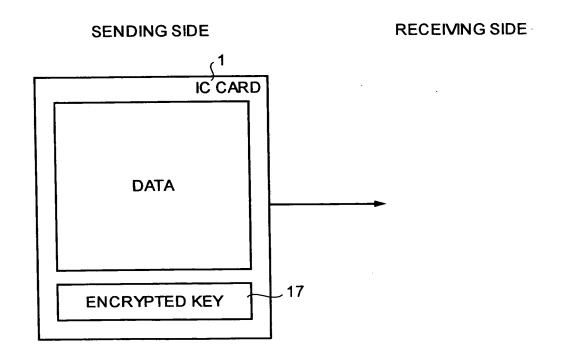
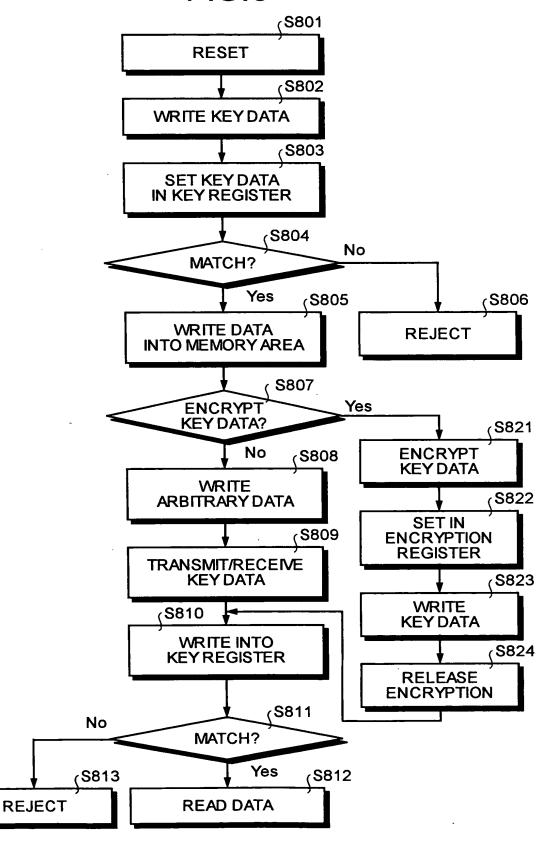


FIG.7



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FIG.8



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FIG.9A

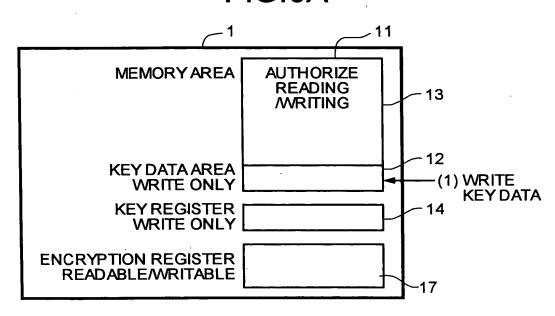
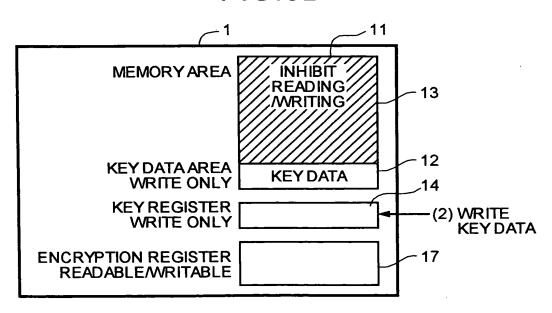


FIG.9B



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FIG.9C

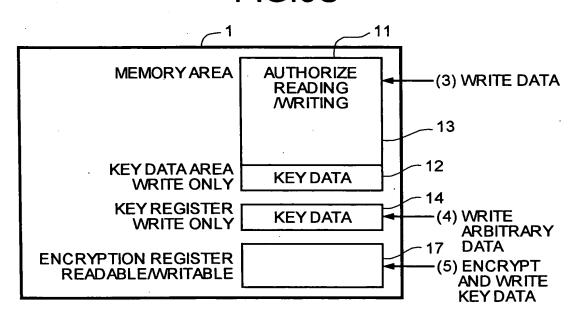
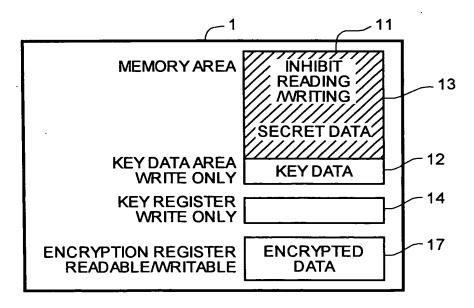


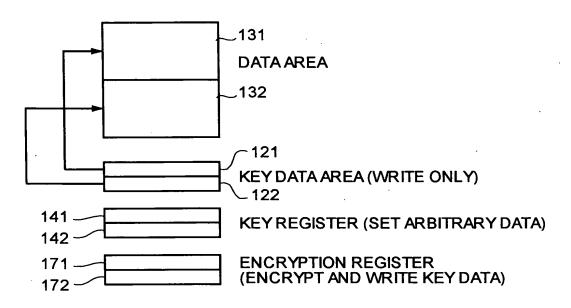
FIG.9D



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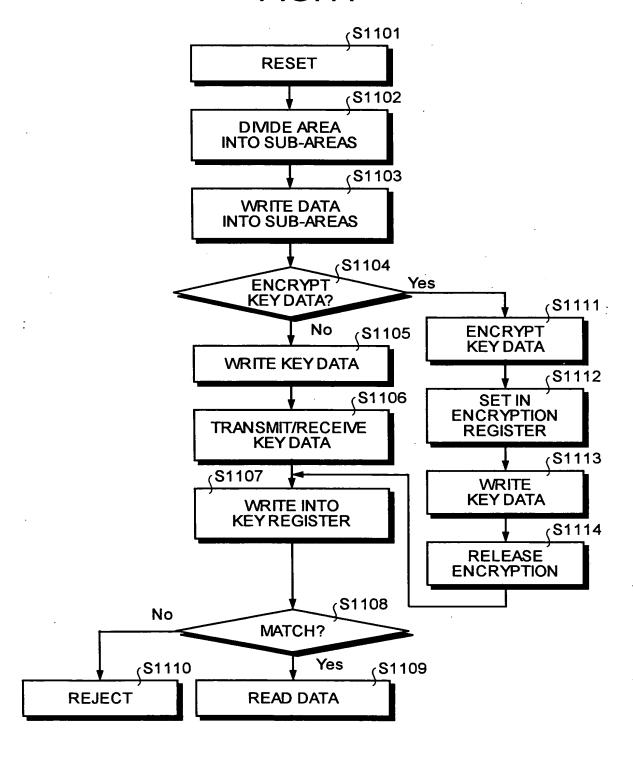
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**FIG.10** 



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**FIG.11** 



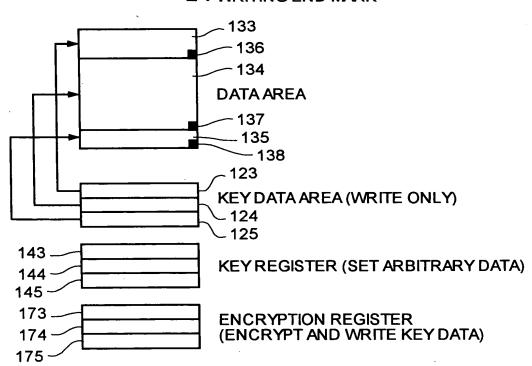
Inventor's Name: HASHIMOTO, et al.

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FIG. 12

## ■: WRITING END MARK



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**FIG.13** 

